

FIG. 1



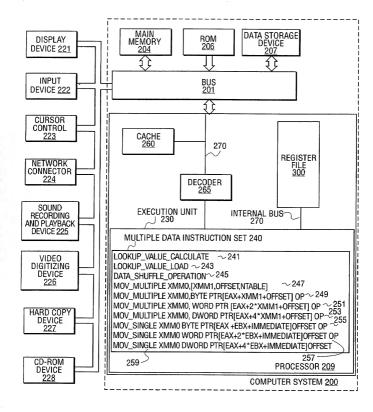
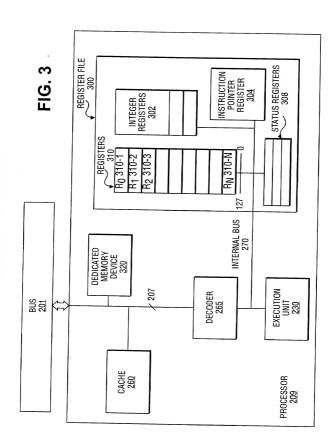


FIG. 2



127 120	119 112	111 104	103		24 23	3 16	15 8	7	0
BYTE 15	BYTE 14	BYTE 13	•	• •	1	BYTE 2	BYTE 1	BYTE	0
PAC	KED BYTE	280							
127	112	111				16	15		0
WO	RD 7		•	• •			wo	RD 0	
PAC	KED WOR	282							
127		96	95		32	31			_0
DOL	JBLE WOR	DВ	•	• •		DOU	BLE WOR	D 0	
PAC	KED DOUB	LE WORD 2	84						

FIG. 4

0	वववव वववव वववव वववव वववव		0	sbbb bbbb sbbb bbbb sbbb	
8.7	pppr		8 7	sppc	
80	qqqq			pppp	
15	qqqq		16 15	qqqs	
16	qqqq	98		pppp	
24 23 16 15	qqqq	JNSIGNED PACKED BYTE IN-REGISTER REPRESENTATION 286	24 23	qqqs	V 288
24		ENTAT	24		IATIOI
	•	RES		•	SEN.
	•	뮨		•	P.B.
	•	STER		•	H. H.
103		92	103		IST
104	pppp	E IN-	104	pppp	N-RE(
Ξ	qqqq	D BYT	#	qqqs	3YTE I
112	qqqq	PACKE	112	qqqq	XED !
119	pppp	SNED I	120 119 112 111 104 103	qqqs	SIGNED PACKED BYTE IN-REGISTER REPRESENTATION 288
120 119 112 111 104 103	qqqq	UNSIG	120	qqqq	SIGNE
127	वववव वववव वववव वववव वववव		127	sobb bobb sobb bobb sobb bobb	

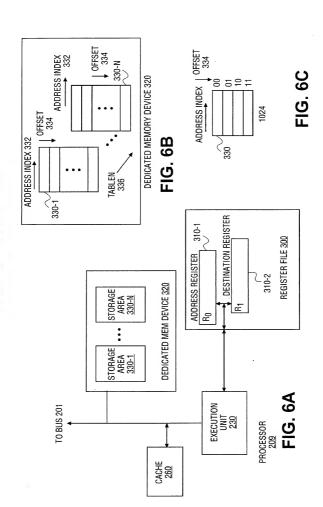
FIG. 5A

127 112 111	Ħ	1615 0
WWWW WWWW WWWW WWWW	•	WWWW WWWW WWWW
UNSIGNED PACKED WORE	UNSIGNED PACKED WORD IN-REGISTER REPRESENTATION 290	
112 111	11	1615 0
SWWW WWWW WWWW WWWW	•	SWWW WWWW WWWW
SIGNED PACKED WORD IN	SIGNED PACKED WORD IN-REGISTER REPRESENTATION 292	

FIG. 5B

127	6 96	96 95 32 31	31	0
)	dada adad adad adad adad adad adad • • • dada adad adad	•	dadd dddd dddd dddd dddd dddd dddd	
	UNSIGNED DOUBLEWORD IN-REGISTER REPRESENTATION 294	TATION 29	4	1
127	6 96	96 95 32 31	31	0
0,	sada dada dada dada dada dada dada dada	•	sada dada dada dada dada dada dada	
	SIGNED DOUBLEWORD IN-REGISTER REPRESENTATION 296	10N 296		1

FIG. 5C



MOV_MULTIPLE R₀, R₁

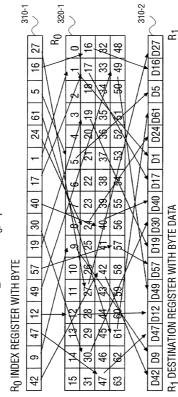
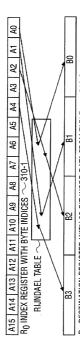


FIG. 7

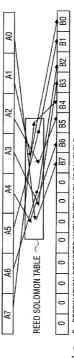




 $_{
m H_1}$ destination register with 32-bit word data from Table. \sim 310-2

FIG. 8

MOV_MULTIPLE R_0 Byte ptr [fax + R_1 + 0ffset] 310-1 \sim $\,$ R_0 index register with 16-bit indices



310-2 \sim R₁ Destination register with Byte data from Table.

FIG. 9

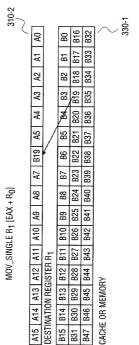


FIG. 10

DATA_SHUFFLE_OP

															020
OXF 0XA 0X	XO	ŏ	ŏ	ŏ	ŏ	ŏ	ŏ	ŏ	š	ŏ	ĕ	ŏ	χo	XO	
XIMMO (BELOW BEFORE BYTE SHUFFLE INSTRUCTION)	LOW I	3EFOR	E BYTE	SHU	F	NSTR	UCTIC	íS.							340
A15 A14 A13 A12 A11 A10 A9 A8 A7 A6 A5 A4 A3 A2 A1	H 413	A12	A11	A10	A9	48	A7	A6	45	44	A3	A 2	H.	Α0	2
_		۱۱	\mathbb{A}	M	M,	K	K	1		$/\!/\!/$	1	1			
+	1	1/4	1	×	¥	×	۷,	/	1	1	1		H	1	5
A15 A10 A9 A8 A11 A9	A9	A8	A11	49	A8	A8 A7 A7 A8 A7 A6 A3 A7 A6	A7	A8	A7	A6	A3	A7	9e	A5	5 A5 310
XMM0 AFTER BYTE SHUFFLE OPERATIONS	IER BY	TE SH	UFFLE	OPER	ATIO	NS									

FIG. 11

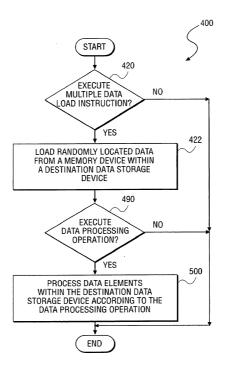


FIG. 12

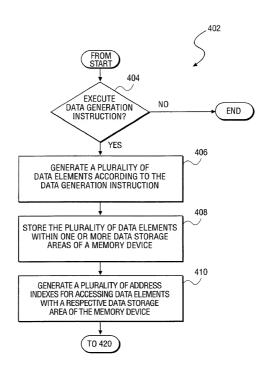
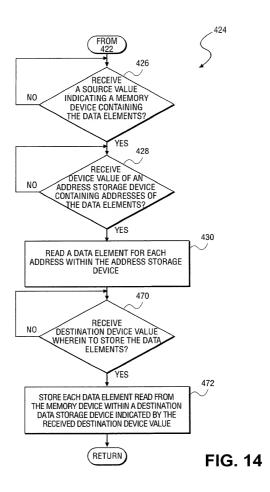
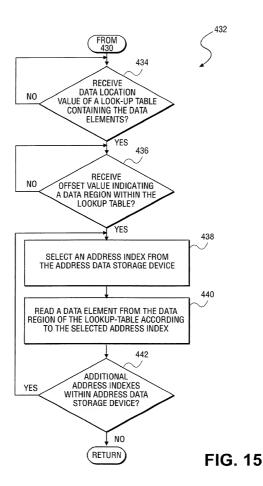
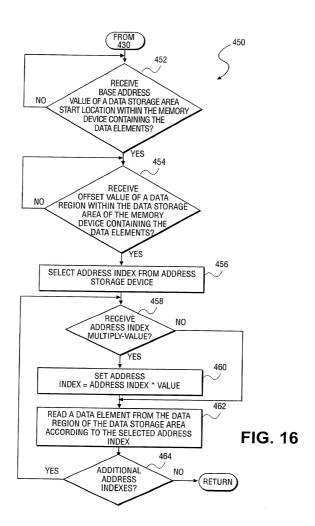


FIG. 13







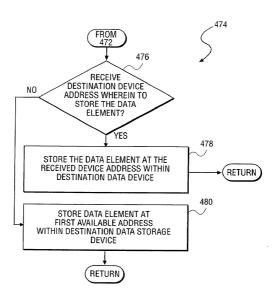


FIG. 17

